

#2/ Cited
Art
g. Prod
4/21/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re DIVISIONAL Application of)
Shunpei YAMAZAKI et al.)
Based On Serial No. 08/757,616) Art Unit: 2812
Which Was Filed: November 29, 1996) Examiner: R. Booth
For: SEMICONDUCTOR DEVICE AND)
METHOD FOR FORMING)
THE SAME) Date: February 23, 1999



INFORMATION DISCLOSURE STATEMENT
AND NOTIFICATION OF RELATED APPLICATION

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

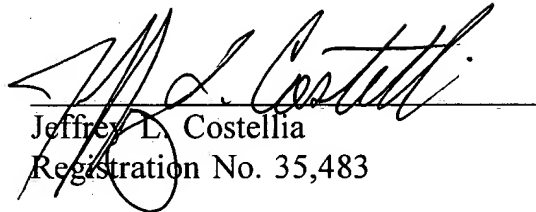
In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 08/757,616 filed, November 29, 1996 and predecessor application Serial No. 08/085,931, filed July 6, 1993.

The attention of the Examiner is directed to related application Serial

No. 09/229,676 filed January 13, 1999.

Respectfully submitted,



Jeffrey L. Costella
Registration No. 35,483

Sixbey, Friedman, Leedom & Ferguson, P.C.
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
(703) 790-9110